	10/070 111	
Notice of Allowability	10/673,444	KONDO, TAKAYUKI
	Examiner	Art Unit
	Quyen P. Leung	2874
The MAILING DATE of this communication appear All claims being allowable, PROSECUTION ON THE MERITS IS (herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGOT THE Office or upon petition by the applicant. See 37 CFR 1.313	OR REMAINS) CLOSED in to or other appropriate commung GHTS. This application is suited.	this application. If not included nication will be mailed in due course. THIS
1. This communication is responsive to <u>1/4/2007</u> .		
2. The allowed claim(s) is/are <u>1 and 3-33</u> .	·	
3.  Acknowledgment is made of a claim for foreign priority unit a)	been received. been received in Application cuments have been received in Application cuments have been received in Application.  of this communication to file at ENT of this application.  tted. Note the attached EXAM is reason(s) why the oath or continuous properties of the submitted.  on's Patent Drawing Review (as Amendment / Comment or in the header according to 37 CFR is it of BIOLOGICAL MATER	in this national stage application from the a reply complying with the requirements  MINER'S AMENDMENT or NOTICE OF declaration is deficient.  ( PTO-948) attached  In the Office action of a drawings in the front (not the back) of a 1.121(d).  RIAL must be submitted. Note the
Attachment(s)  1. ☑ Notice of References Cited (PTO-892)  2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)  3. ☐ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date  4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	5. ☐ Notice of Info 6. ☐ Interview Sur Paper No./M 7. ⊠ Examiner's A	ormal Patent Application

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## **EXAMINER'S AMENDMENT**

In response to applicant's amendment filed 1/4/2007, claims 1, 3, 7, 9-15, 22-25, and 27-31 have been amended. Applicant's arguments filed on 1/4/2007 have been considered. Claims 1, 3-33 are pending and allowed.

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Mr. Gang Luo on 3/2/2007.

The application has been amended as follows:

- Claim 24 line 4 "the micro tile element" has been changed to –at least one
  of the first and second elements—for proper antecedent basis.
- Claim 25 line 4 "the micro tile element" has been changed to –at least one
  of the first and second elements--, for proper antecedent basis.

The following is an examiner's statement of reasons for allowance:

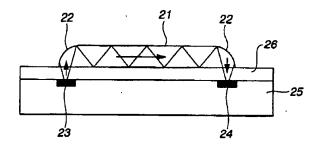
The closest prior art, Sakata et al (6,868,214 and JP 2001-042150 A) illustrates an optical interconnection circuit and the method of making the circuit. In figure 2 comprising a substrate(25) on which a first element (23) and a second element (24) are formed and an optical waveguide (21) for transmitting an optical signal emitted from the first element (23) to the second element (24). Sakata et al does not anticipate or make

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obvious applicant's claimed invention, because Sakata et al does not teach (1) the elements being bonded or fixed by an adhesive to the substrate and (2) the optical waveguide (21) contacting the part of the first element and the part of the second element. Sakata's figure 2 shows an intervening layer (26) between the waveguide (21) and the elements (23,24). Sakata's col. 5 lines 40-65 teaches that elements (23,24) are formed on the substrate (25):

FIG.2



FIGS. 2 and 3 respectively illustrate typical simple optical interconnection devices in which a waveguide 21 with end portions 22 formed on a cladded plate 26 is provided on a substrate 25 or 27. In the structure of FIG. 2, a light emitting device 23 and a light receiving device 24 are formed on a surface of the substrate 25, and this surface of the substrate 25 is bonded to the cladded plate 26 with positions of the devices 23 and 24 aligned with the respective end portions 22 of the waveguide 21. In the structure of FIG. 3, a light emitting device 23 and a light receiving device 24 are formed on a surface of the substrate 27, and the other surface of the substrate 27 is bonded to the cladded plate 26 with positions of the devices 23 and 24 aligned with the respective end portions 22 of the waveguide 21.

The end portion 12 or 22 has an approximately spherical profile, so that this profile acts as a concave mirror. Thereby, light emitted from the light emitting device 23 perpendicularly to the cladded plate 26 can readily couple to the waveguide 11 or 21 through the end portion 12 or 22, and light converged by the end portion 12 or 22 can readily couple to the light receiving device 24.

A waveguide of the present invention can be fabricated by using, as a mold for forming a waveguide, a substrate with a partial cylindrical recess formed by a method of fabricating a partial cylindrical structure.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Quyen P. Leung whose telephone number is (571) 272-8188. The examiner can normally be reached on normally M-F, 6:15 am - 2:45 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rodney Bovernick can be reached on (571) 272-2344. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

Quyen Leung

Primary Patent Examiner

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qpl